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CLIENT NO.: STMI09-02369
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Rajeev Srivastavaan et al.
Application No. : 10/804,562
Filed : March 19, 2004
For : CONTENT ADDRESSABLE MEMORY ARCHITECTURE
PROVIDING IMPROVED SPEED
Art Unit : 2827
Examiner : Huan Hoang

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents herein are listed below and on the attached Form PTO/SB/08A.


<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
6,044,005	Gibson et al.	March 28, 2000
6,324,087	Pereira	November 27, 2001
6,362,993	Henderson et al.	March 26, 2002
6,373,738	Towler et al.	April 16, 2002
6,452,822	Chai et al.	September 17, 2002
6,560,670	Ichiriu	May 6, 2003
6,829,153	Park et al.	December 7, 2004

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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Date: July 7, 2005



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